TOSHIBA Photocoupler GaAs Ired & Photo-Triac

TLP161G

Triac Drive
Programmable Controllers
AC-Output Module
Solid State Relay

The TOSHIBA mini flat coupler TLP161G is a small outline coupler, suitable for surface mount assembly.

The TLP161G consists of a photo triac, optically coupled to a gallium arsenide infrared emitting diode.

• Zero-voltage crossing turn-on

• Peak off-state voltage: 400V(min.)

• Trigger LED current: 10mA(max.)

• On-state current: 70mA(max.)

• Isolation voltage: 2500Vrms(min.)

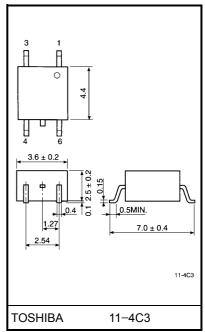
• UL recognized: UL1577, file no. E67349

Trigger LED Current

| Classi– fication* | Trigger LED Current (mA) | | Marking Of | | |
|----------------------|-----------------------------|------|---------------------------|--|--|
| | V _T =3V, Ta=25°C | | Marking Of Classification | | |
| | Min. | Max. | Classification | | |
| (IFT5) | _ | 5 | T5 | | |
| (IFT7) | _ | 7 | T5, T7 | | |
| Standard | _ | 10 | T5, T7, blank | | |

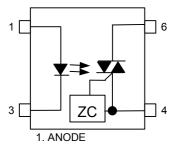
*Ex. (IFT5); TLP161G(IFT5)

(Note) Application type name for certification test, please use standard product type name, i.e. TLP161G(IFT5): TLP161G Unit in mm



Weight: 0.09 g

Pin Configurations



- 3. CATHODE
- 4. TERMINAL 1
- 6. TERMINAL 2

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Maximum Ratings (Ta = 25°C)

| Characteristic | | | Symbol | Rating | Unit | |
|---|--|----------------------|---------------------|---------|------|--|
| | Forward current | lF | 50 | mA | | |
| | Forward current derating (Ta | ΔI _F / °C | -0.7 | mA / °C | | |
| LED | Peak forward current (100µs p | oulse, 100pps) | I _{FP} | 1 | Α | |
| | Reverse voltage | | V _R | 5 | V | |
| | Junction temperature | Tj | 125 | °C | | |
| | Off-state output terminal volta | V_{DRM} | 400 | V | | |
| | On-state RMS current | Ta=25°C | l±(DMO) | 70 | mA | |
| Detector | | Ta=70°C | I _{T(RMS)} | 40 | | |
| | On-state current derating (Ta | ΔI _T / °C | -0.67 | mA / °C | | |
| | Peak on-state current (100µs | I _{TP} | 2 | Α | | |
| | Peak nonrepetitive surge curre (PW=10ms, DC=10%) | I _{TSM} | 1.2 | Α | | |
| | Junction temperature | Tj | 115 | °C | | |
| Storage temperature range | | | T _{stg} | -55~125 | °C | |
| Operating temperature range | | | T _{opr} | -40~100 | °C | |
| Lead soldering temperature (10s) | | | T _{sol} | 260 | °C | |
| Isolation voltage (AC, 1min., R.H.≤ 60%) (Note) | | | BVS | 2500 | Vrms | |

(Note) Device considered a two terminal device: Pins 1 and 3 shorted together and pins 4 and 6 shorted together.

Recommended Operating Conditions

| Characteristic | Symbol | Min. | Тур. | Max. | Unit |
|-----------------------|------------------|------|------|------|------|
| Supply voltage | V_{AC} | _ | _ | 120 | Vac |
| Forward current | I _F | 15 | 20 | 25 | mA |
| Peak on-state current | I _{TP} | _ | _ | 1 | Α |
| Operating temperature | T _{opr} | -25 | _ | 85 | °C |

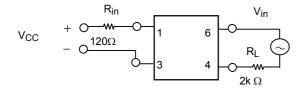
Individual Electrical Characteristics (Ta = 25°C)

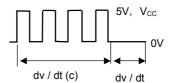
| Characteristic | | Symbol | Test Condition | | Min. | Тур. | Max. | Unit |
|----------------|--|------------------|--|--------|------|------|------|--------|
| LED | Forward voltage | V _F | I _F =10mA | | 1.0 | 1.15 | 1.3 | V |
| | Reverse current | I _R | V _R =5V | | _ | _ | 10 | μA |
| | Capacitance | C _T | V=0, f=1MHz | | - | 30 | 1 | pF |
| Detector | Peak off-state current | I _{DRM} | V _{DRM} =400V | | _ | 10 | 1000 | nA |
| | Peak on-state voltage | V _{TM} | I _{TM} =70 mA | | - | 1.7 | 2.8 | ٧ |
| | Holding current | lΗ | _ | | - | 0.6 | - | mA |
| | Critical rate of rise of off–state voltage | dv / dt | V _{in} =120Vrms, Ta=85°C (F | Fig.1) | 200 | 500 | 1 | V / µs |
| | Critical rate of rise of commutating voltage | dv / dt(c) | V _{in} =30Vrms, I _T =15mA (F | Fig.1) | _ | 0.2 | _ | V / µs |

Coupled Electrical Characteristics (Ta = 25°C)

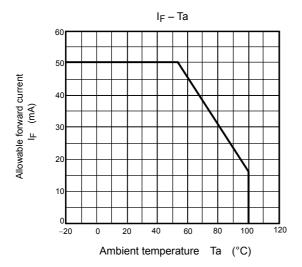
| Characteristic | Symbol | Test Condition | Min. | Тур. | Max. | Unit |
|-------------------------------|-----------------|---|--------------------|------------------|------|------|
| Trigger LED current | I _{FT} | V _T =3V | | 5 | 10 | mA |
| Inhibit voltage | V _{IH} | I _F =rated I _F T | _ | _ | 40 | V |
| Leakage in inhibited state | lін | I _F =rated I _{FT} V _T =rated V _{DRM} | - | 100 | 300 | μΑ |
| Capacitance (input to output) | C _S | V _S =0, f=1MHz | _ | 0.8 | _ | pF |
| Isolation resistance | R _S | V _S =500V, R.H.≤ 60% | 1×10 ¹² | 10 ¹⁴ | _ | Ω |
| | BVS | AC, 1 minute | 2500 | _ | _ | Vrms |
| Isolation voltage | | AC, 1 second, in oil | _ | 5000 | _ | |
| | | DC, 1 minute, in oil | _ | 5000 | _ | Vdc |

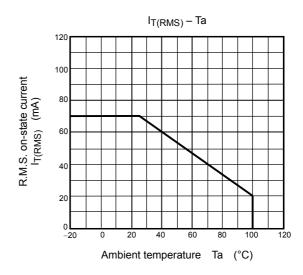
Fig.1 dv / dt test circuit

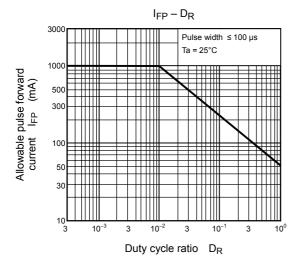


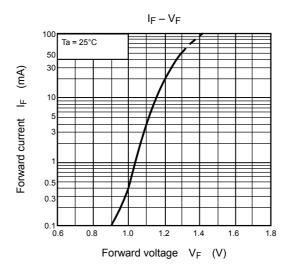


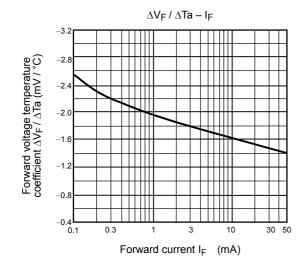
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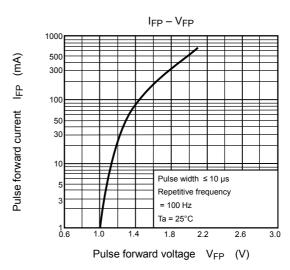




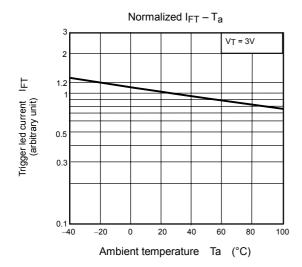


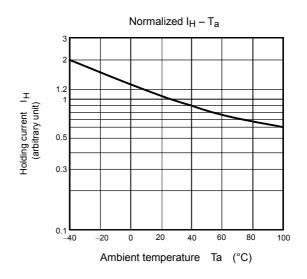


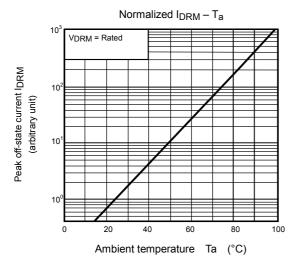


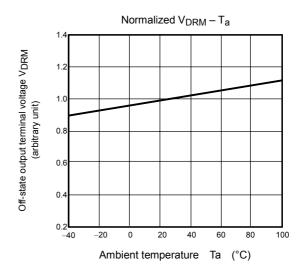


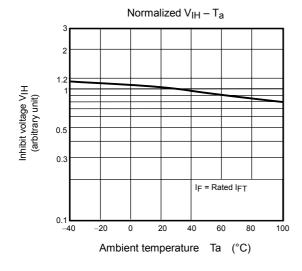
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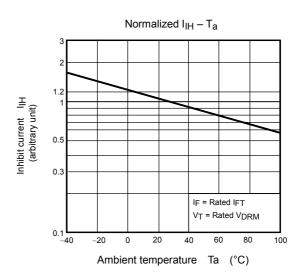












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